FIG. 1 (PRIOR ART)

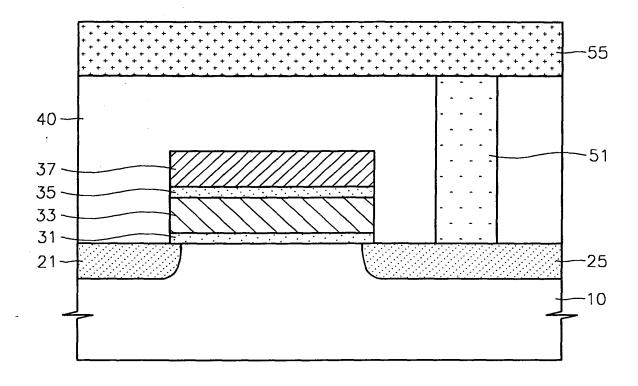


FIG. 2 (PRIOR ART)

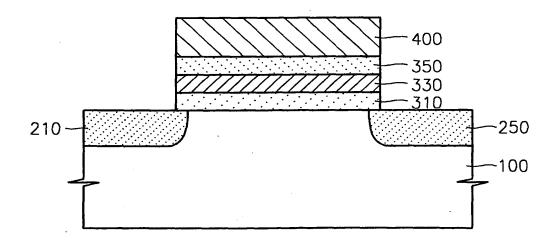
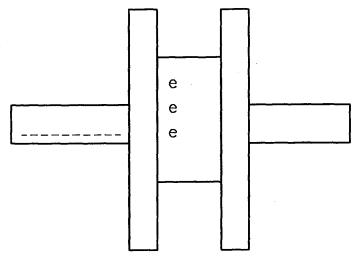


FIG. 3 (PRIOR ART)



Si SUBSTRATE//SiO₂/Si₃N₄/SiO₂//GATE

FIG. 4

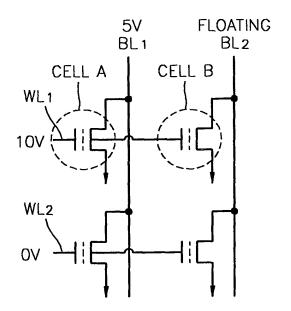


FIG. 5

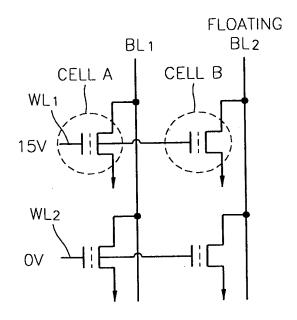


FIG. 6

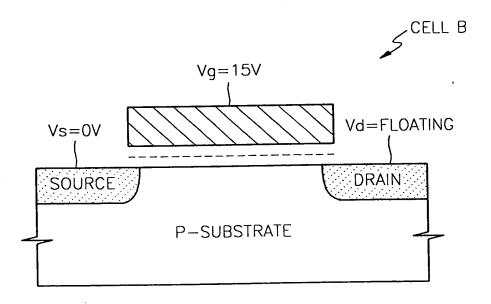


FIG. 7

Vg=10V

SUBSTRATE Si304 CATE
Si02 Si02

FIG. 8

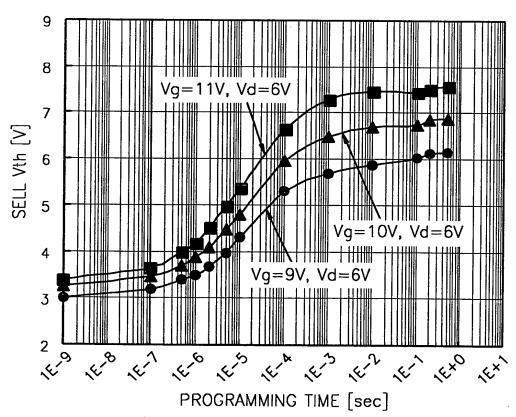


FIG. 9

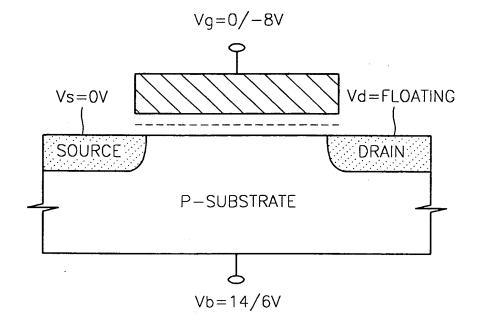


FIG. 10

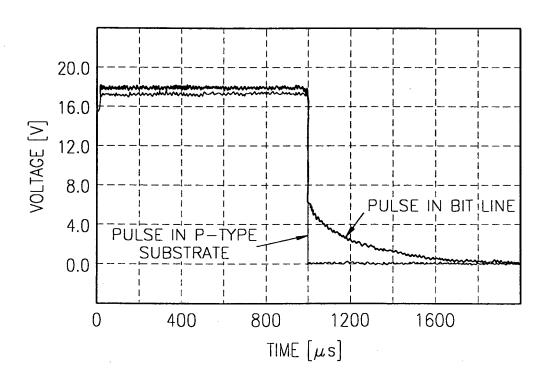


FIG. 11

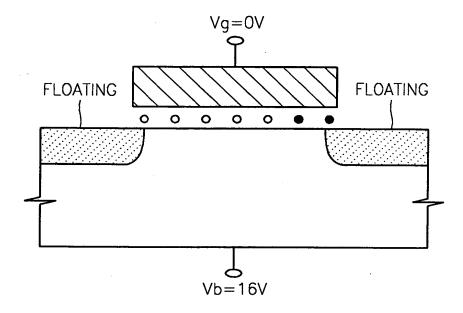


FIG. 12

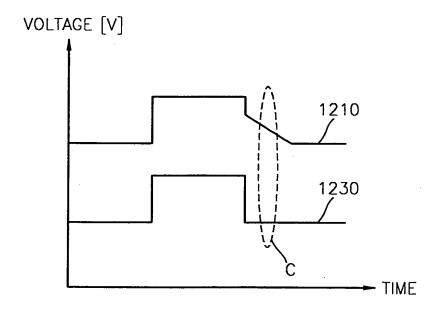


FIG. 13

